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Interference-free microprocessor

Application Number	01103000	Application Date	2001.02.14
Publication Number	1309351	8	2001.08.22
Priority Information	2000/2/14 JP 03	2000/2/14 JP 035898/2000;2000/5/8 JP 135010/2000	/2000
International Classification	G06F9/48;H04L9/00	G06F9/48;H04L9/00	
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Address		and the second	ANNA MATA ANNA ANNA ANNA ANNA ANNA ANNA
Inventor(s) Name	Hashimoto Mikio	ito takeshi	elephone de escritor en espision de principal de espision de la companya del companya de la companya del companya de la companya del la companya de la compa
Patent Agency Code	11038 Patent Ag	Patent Agent	wu IIIi
Abstract	A CA CARAGONIA DE CATA CATA COMPANIA DE CATA CATA CATA CATA CATA CATA CATA CAT		mary version of the second

Under a multi-task environment, a tamper resistant microprocessor saves a context information for one program whose execution is to be interrupted, where the context information contains information indicating an execution state of that one program and the execution code encryption key of that one program. An execution of that one program can be restarted by recovering the execution state of that one program from the saved context information. The context information can be encrypted by using the public key of the microprocessor, and then decrypted by using the secret key of the microprocessor.

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